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THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: KIMURA

PATENT NO: 6,900,689 B2

ISSUED: May 31, 2005

FOR: CMOS REFERENCE VOLTAGE CIRCUIT

GROUP: 2816

CONFIRMATION NO: 8922

EXAMINER: TERRY LEE ENGLUND

DOCKET: NEC NEG-244

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**  
**FEB 13 2006**  
**of Correction**

**PETITION FOR CERTIFICATE OF CORRECTION**

Dear Sirs:

Kimura, the Patentee of the above-identified patent, through his attorney, hereby petitions for issuance of a Certificate of Correction in the above identified patent. A Certificate of Correction (PTO form 1050) is enclosed, in duplicate. The Certificate of Correction is required to correct significant printing errors occurring in the claims as follows:

Claim 4, Col. 24, line 8, "translator" should be -- transistors --.

Claim 4, Col. 24, line 15, "tint" should be -- first --.

Claim 12, Col. 26, line 41, "arid" should be -- and --.

Claim 17, Col. 29, line 3, "value equal to" should be -- value different from --.

Since the errors were Patent Office errors, it is believed that the Certificate of Correction should be issued without charge to the Applicant. A correct copy of the claims appear in Amendment D under rule 116.

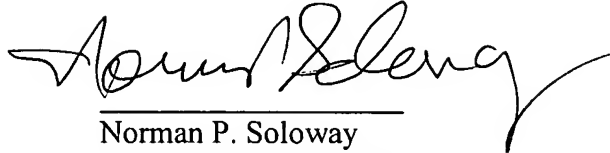
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**FEB 13 2006**

B2

Respectfully submitted,



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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 2, 2006 at Tucson, AZ.

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FEB 13 2006

# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,900,689 *02*

DATED : May 31, 2005

INVENTOR(S) : KIMURA

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 4, Col. 24, line 8, "translator" should be -- transistors --.

Claim 4, Col. 24, line 15, "tint" should be -- first --.

Claim 12, Col. 26, line 41, "arid" should be -- and --.

Claim 17, Col. 29, line 3, "value equal to" should be -- value different from --.

MAILING ADDRESS OF SENDER (Please do not use customer number

PATENT NO. 6,900,689

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FEB 13 2006

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,900,689 *B2*

DATED : May 31, 2005

INVENTOR(S) : KIMURA

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 4, Col. 24, line 8, "translator" should be -- transistors --.

Claim 4, Col. 24, line 15, "tint" should be -- first --.

Claim 12, Col. 26, line 41, "arid" should be -- and --.

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PATENT NO. 6,900,689

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